

## Using the DAK-002 with an ASIC/2-8040

Effects: ASIC/2-8040 Devices

Date: May 28, 1998

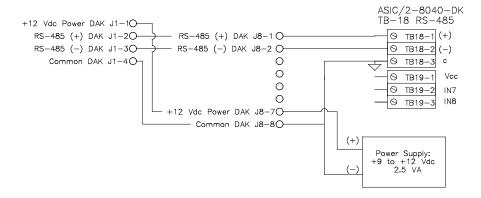


Note No. TE001

The DAK-002 is traditionally used with the ASIC/2-7040 controller. For our OEM customers we have developed firmware for the ASIC/2-8040 that supports the DAK on its local bus.

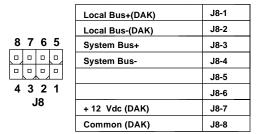
The configuration can be done via SET7040 or the 24-DSM\*\*.SCP and 25-DSL\*\*.SCP script files can be placed in the SCP\_8040 sup-directory of SET8040.

The wiring diagram below explains how to connect the DAK-002 to an ASIC/2-8040. Before the controller can be configured to support the DAK a "B" version of EPROM must be inserted into the controller.



J1-1 thru J1-4 are connections at the DAK-002. J8-1 thru J8-8 is a mating female connector (similar to the one on the ASIC/2-7040). TB18-1 thru TB18-3 and TB19-1 thru TB-3 are connections at the 8040. Below is a diagram that descibes the J8 connector.

## DAK Communication(FW740A)



The diagram of J8 is a front view of the female connector. The "notches" should help with referencing.

Powering the DAK requires 12Vdc at 250mA maximum. The 12Vdc is received external to the board form a 3<sup>rd</sup> party power supply.